

FORMAL DRAWINGS

◎ 亂世之亂世

10614

FIG. 1

100

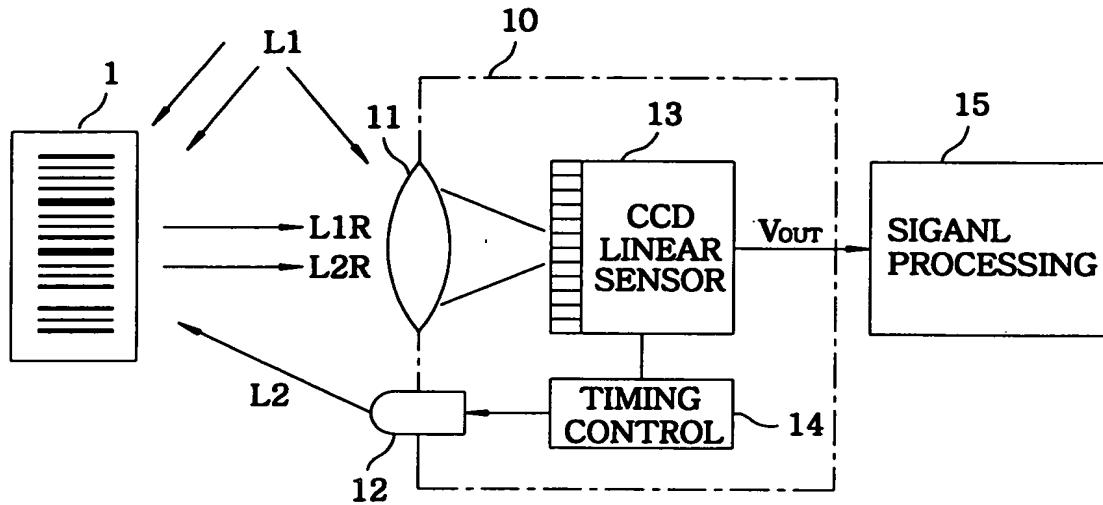
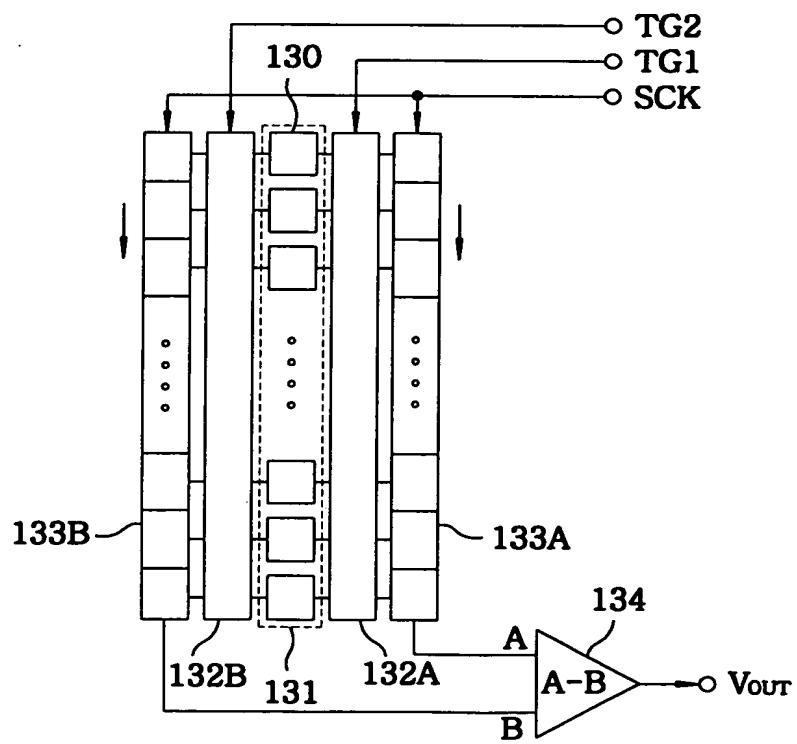


FIG. 2

13



2 26 14

FIG. 3A OPERATION MODE

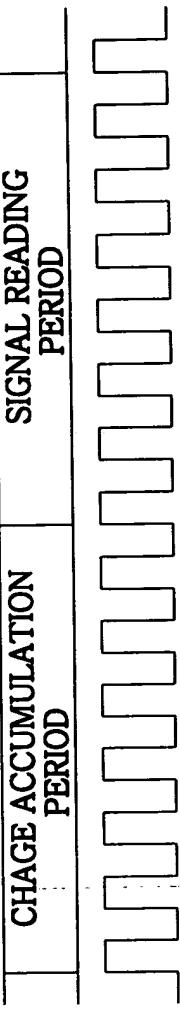


FIG. 3B ON/OFF CONTROL SIGNAL



FIG. 3C EXPOSURE PERIODS OF OFF-STATE



FIG. 3D TRANSFER TIMING (TG1)
FOR FIRST TRANSFER GATE



FIG. 3E TRANSFER TIMING (TG2)
FOR SECOND TRANSFER GATE

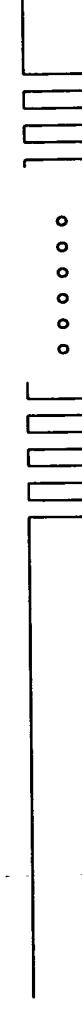


FIG. 3F SIGNAL READING TIMING (SCK)

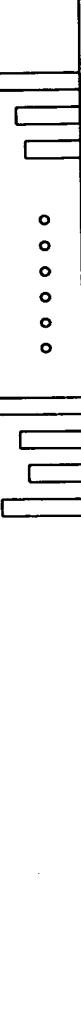


FIG. 3G OUTPUT SIGNAL(V_{out})

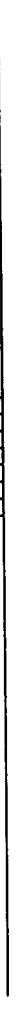


FIG. 4A

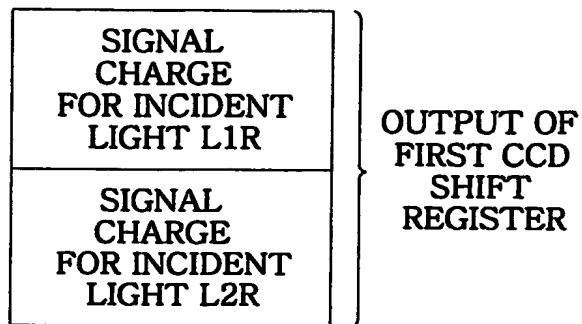


FIG. 4B

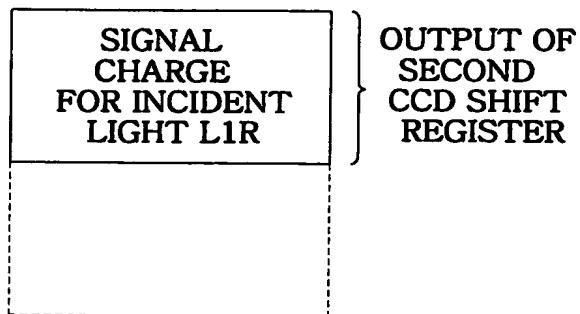
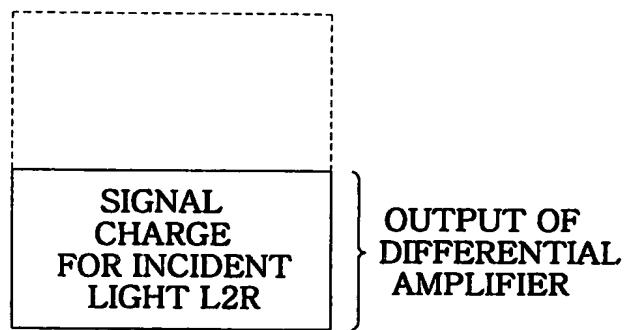


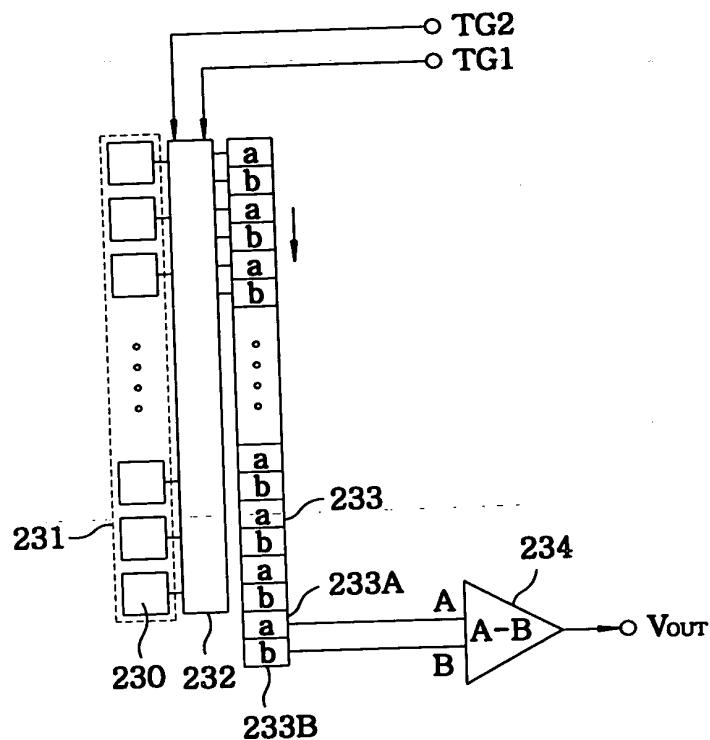
FIG. 4C



4 of 14

FIG. 5

23



09215623-111800

FIG. 6A SIGNAL READING
TIMING FOR CCD 233

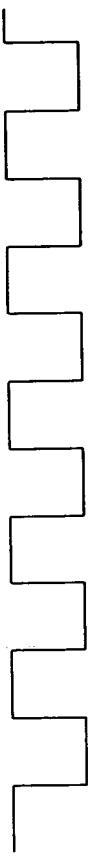


FIG. 6B OUTPUT SIGNAL
OF CCD 233A

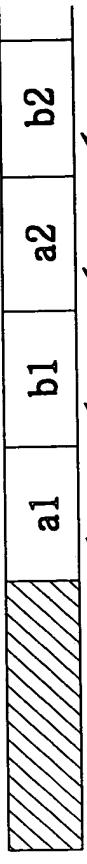


FIG. 6C OUTPUT SIGNAL
OF CCD 233B

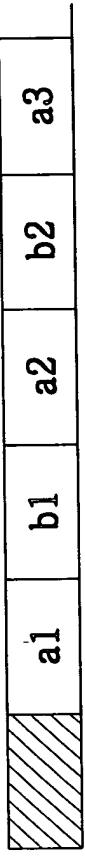


FIG. 6D OUTPUT SIGNAL
OF DIFFERENTIAL
AMPLIFIER

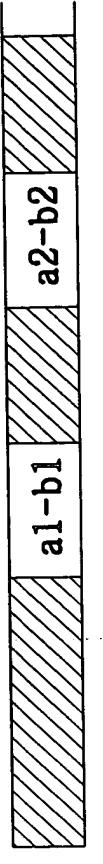
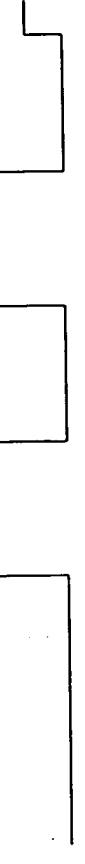
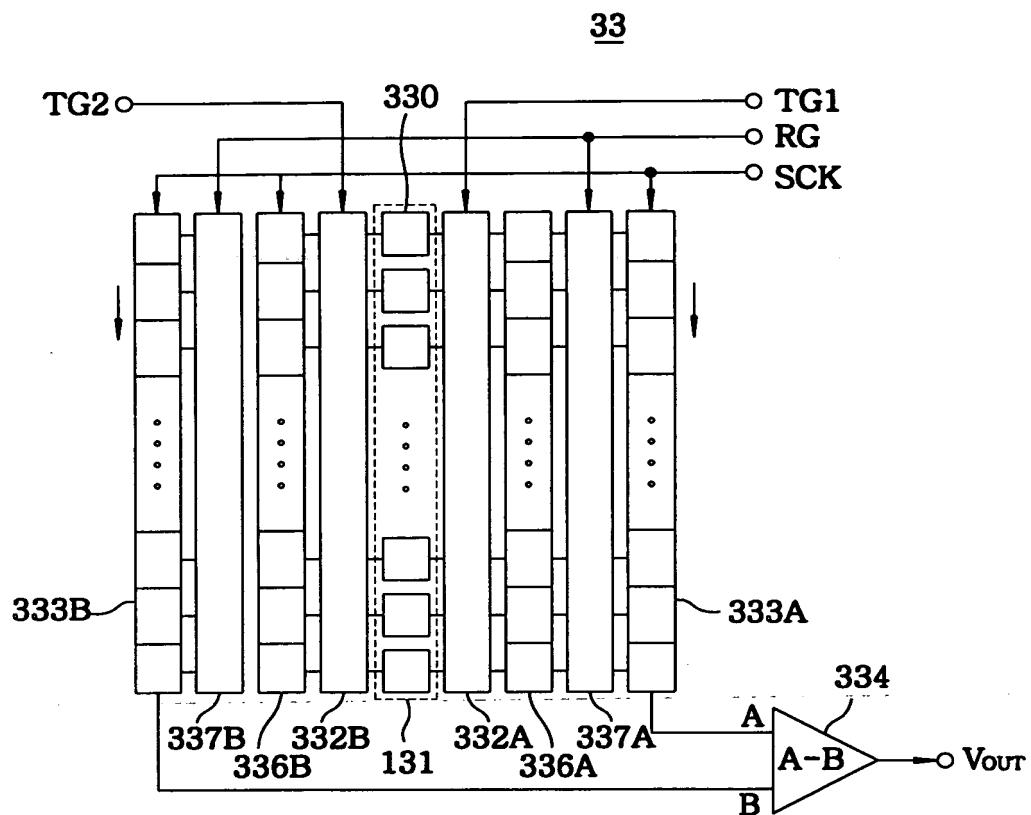


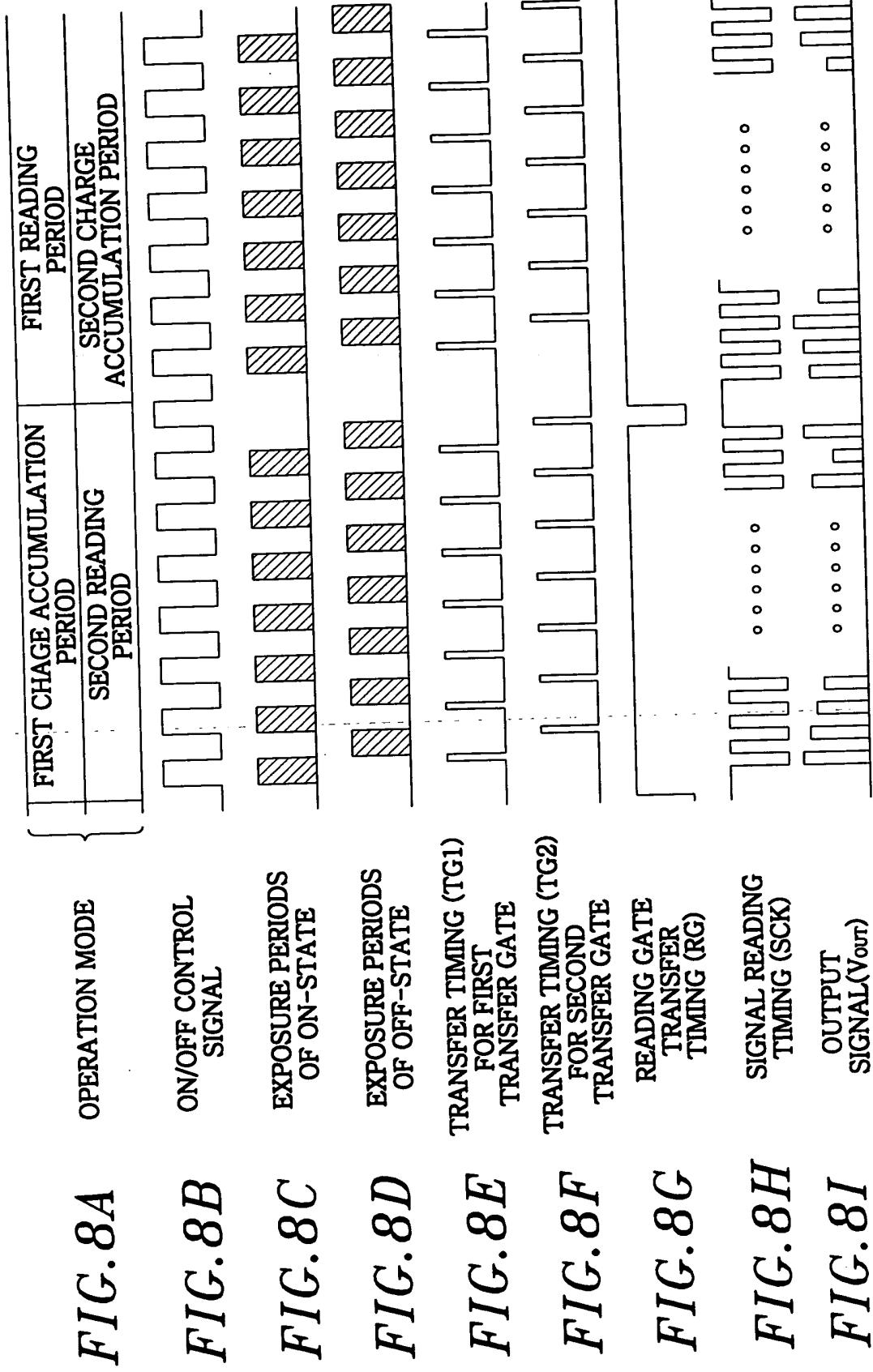
FIG. 6E OUTPUT VOLTAGE
OF DIFFERENTIAL
AMPLIFIER



6/6/14

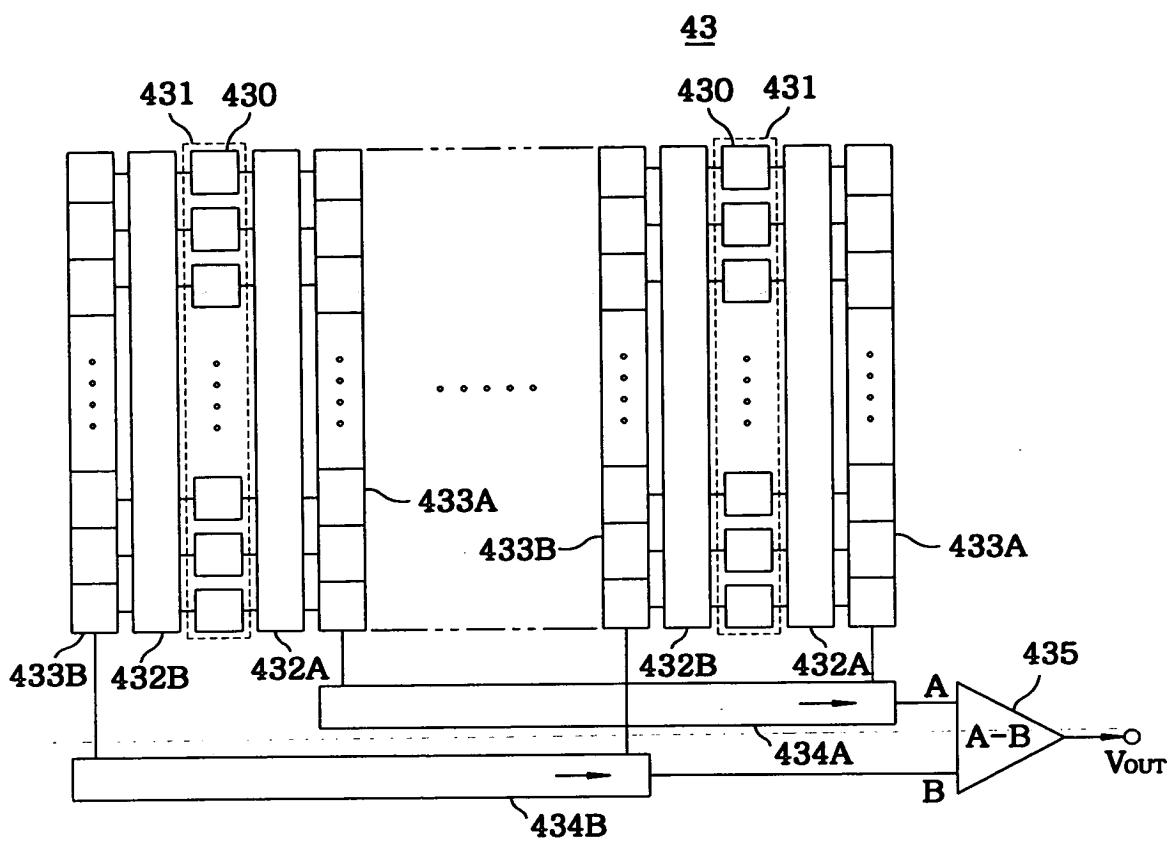
FIG. 7





8 814

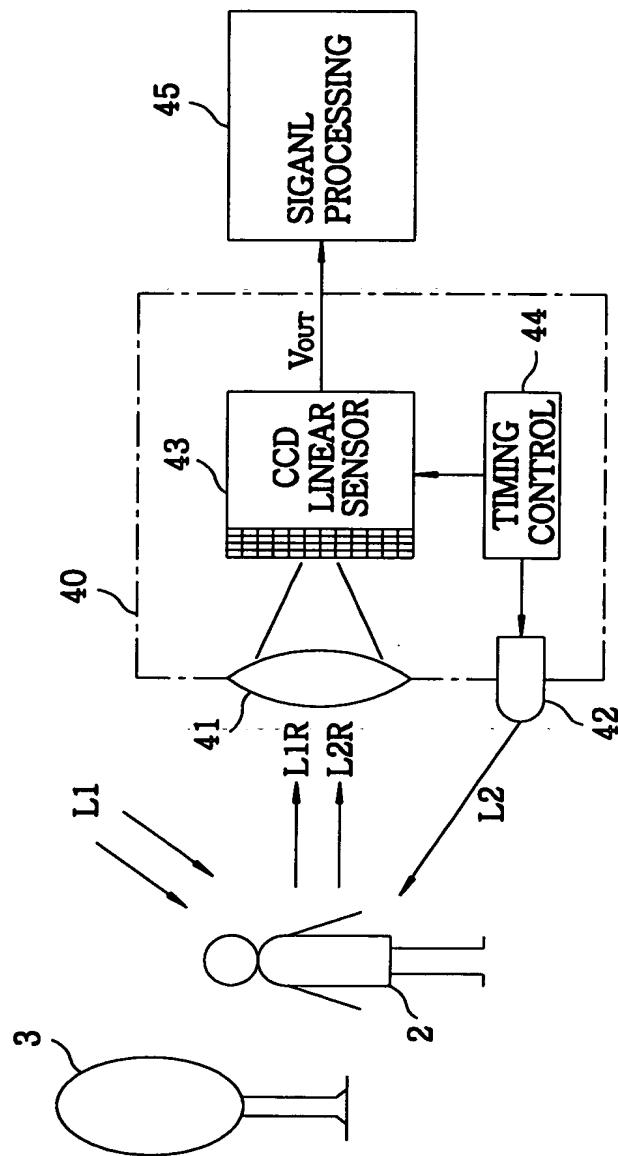
FIG. 9



09215529 * 1418000

28/14

FIG. 10



10 of 14

FIG. 11A

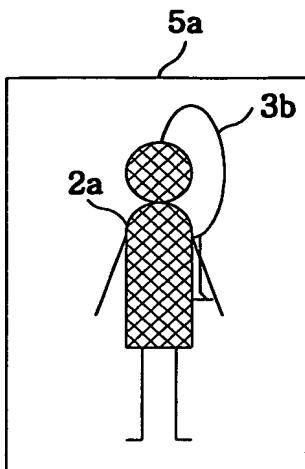


FIG. 11B

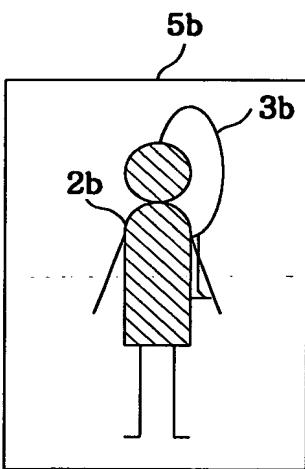
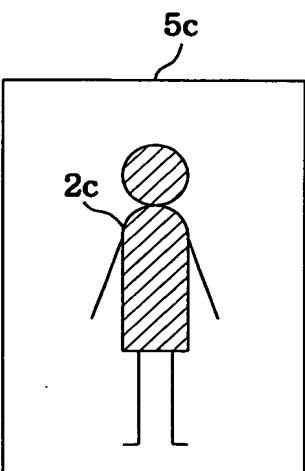


FIG. 11C



11/2/14

FIG. 12

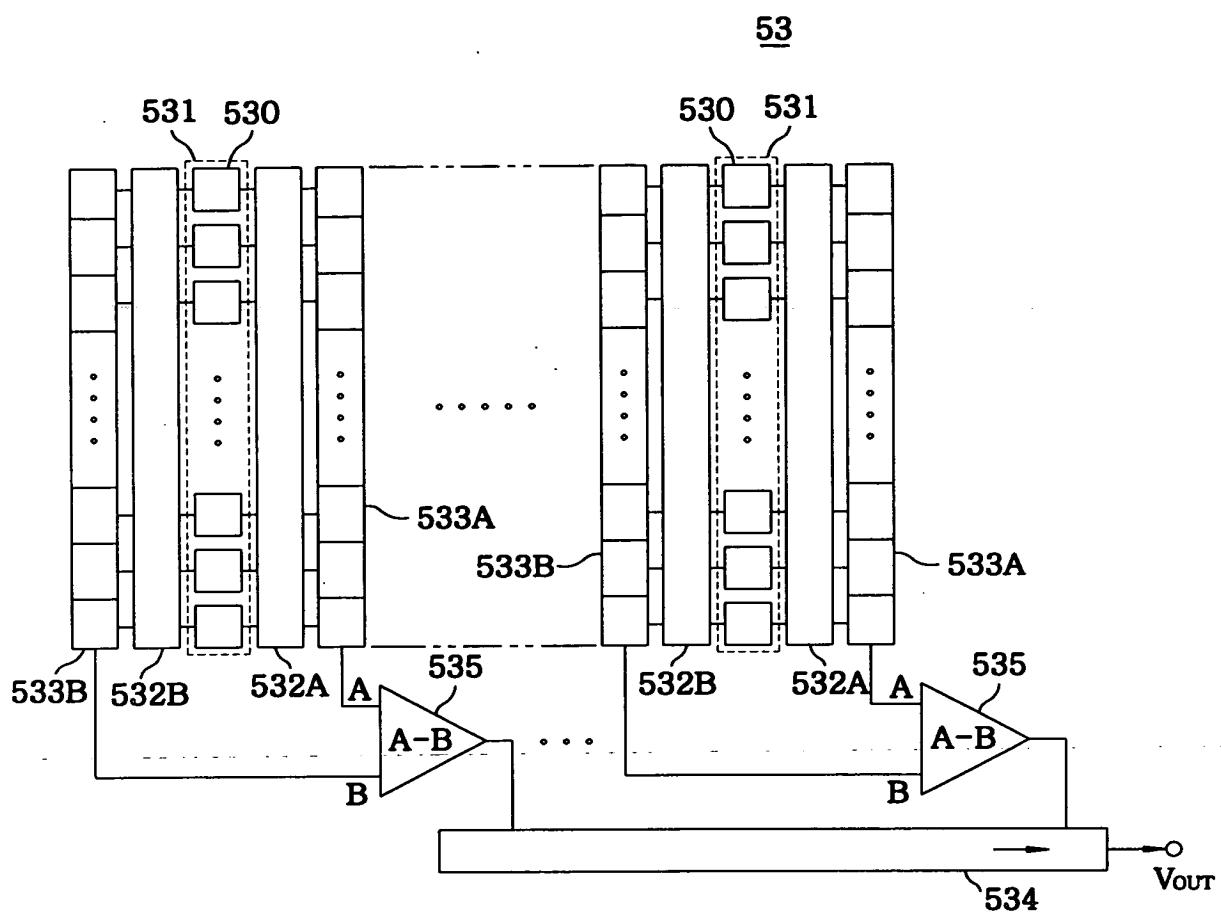


FIG. 13A OPERATION MODE

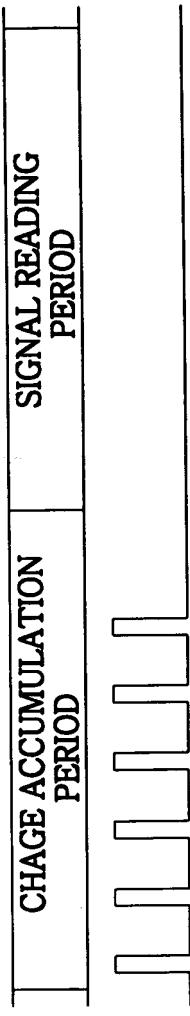


FIG. 13B ON/OFF CONTROL SIGNAL

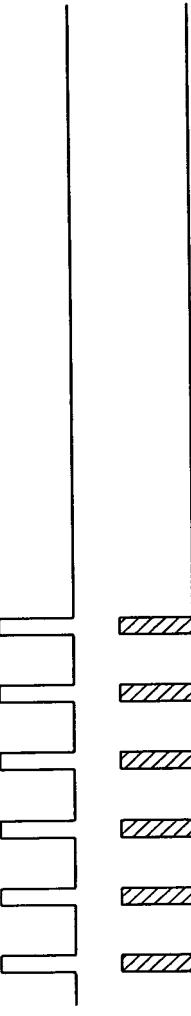


FIG. 13C EXPOSURE PERIODS OF OFF-STATE

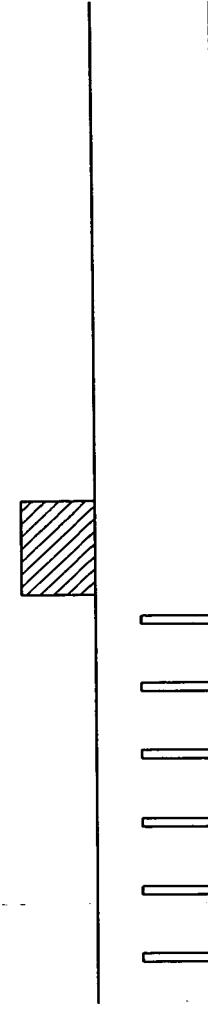


FIG. 13E TRANSFER TIMING FOR FIRST TRANSFER GATE

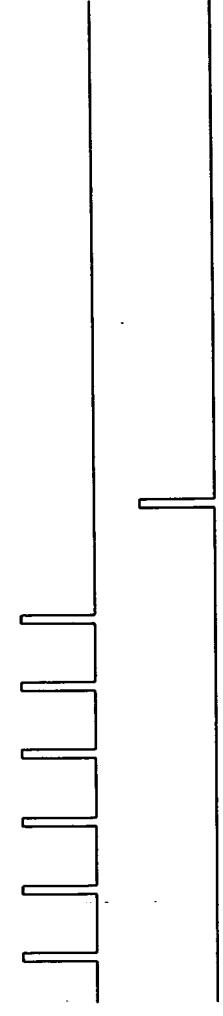


FIG. 13F TRANSFER TIMING FOR SECOND TRANSFER GATE

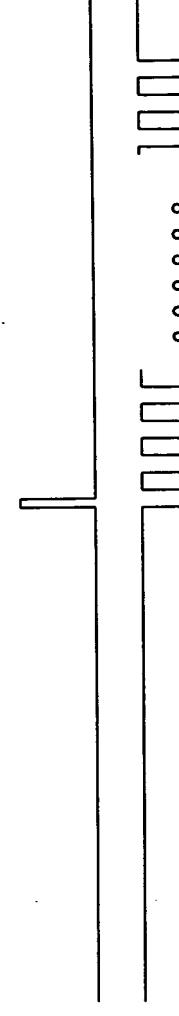


FIG. 13G SIGNAL READING TIMING (SCK)

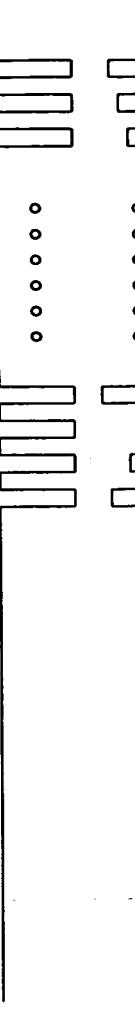
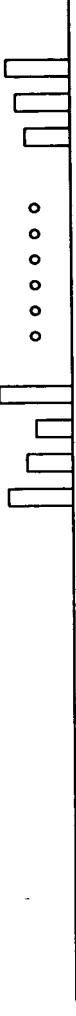


FIG. 13H OUTPUT SIGNAL(V_{out})



13 88 14

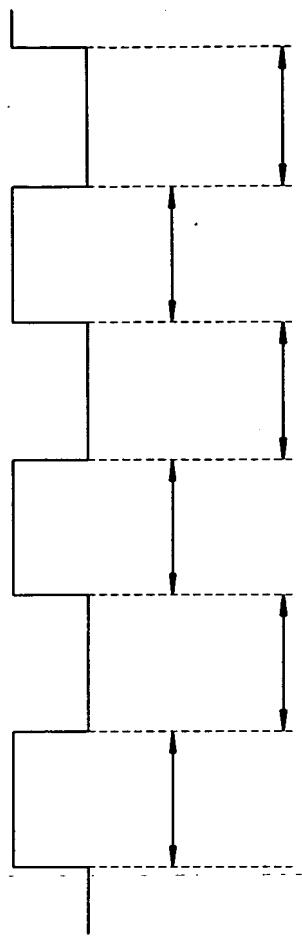


FIG. 14A
ON/OFF
OPERATION(50%)

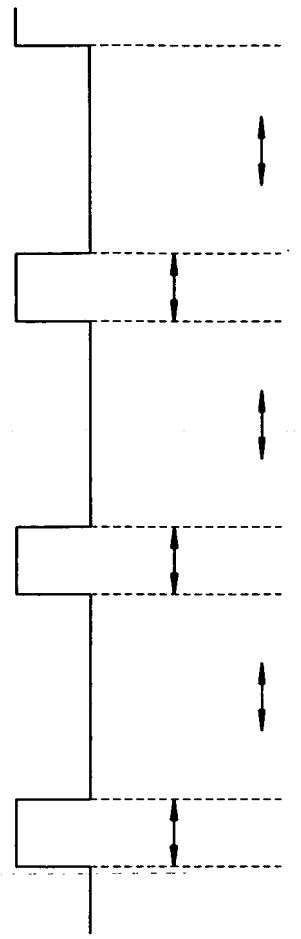


FIG. 14B
ON/OFF
OPERATION(25%)

FIG. 14C
EXPOSURE PERIODS
OF ON-STATE

FIG. 14C
EXPOSURE PERIODS
OF OFF-STATE

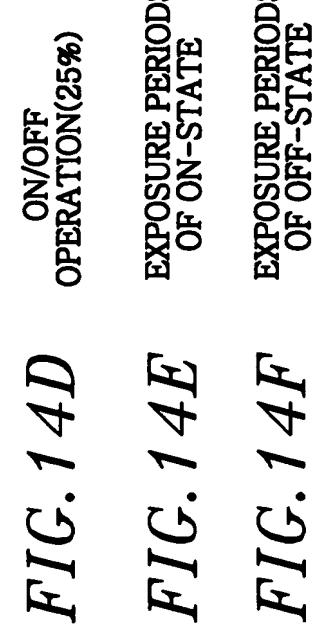


FIG. 14D
ON/OFF
OPERATION(25%)

FIG. 14E
EXPOSURE PERIODS
OF ON-STATE

FIG. 14F
EXPOSURE PERIODS
OF OFF-STATE

FIG. 15

